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CONFIRMATION NO. 79

Bib Data Sheet

SERIAL NUMBER 10/671,474	FILING DATE 09/29/2003 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DCKE NO. HITA.0440
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APPLICANTS

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** CONTINUING DATA ***** *Yes ID*

This application is a CON of 10/173,035 06/18/2002 PAT 6,653,868

** FOREIGN APPLICATIONS ***** *Yes ID*

JAPAN 2001-216387 07/17/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 12/18/2003

Foreign Priority claimed

☒ yes ☐ no

35 USC 119 (a-d) conditions met

☒ yes ☐ no ☐ Met after Allowance

Verified and Acknowledged

Examiner's Signature *Murando*Initials *ID*

STATE OR

COUNTRY
JAPAN

SHEETS

DRAWING
7

TOTAL

CLAIMS
7

INDEPENDENT

CLAIMS
2

ADDRESS

38327

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TITLE

Semiconductor integrated circuit, method and program for designing the semiconductor integrated circuit

FILING FEE

RECEIVED
750

FEES: Authority has been given in Paper
 No. _____ to charge/credit DEPOSIT ACCOUNT
 No. _____ for following:

☐ All Fees☐ 1.16 Fees (Filing)☐ 1.17 Fees (Processing Ext. of time)☐ 1.18 Fees (Issue)☐ Other _____